Master of Science
Integrated Circuit Design

At A Glance

JOINT DEGREE BY
Technische Universität München (TUM)
Nanyang Technological University (NTU)

20 MONTHS FULL TIME PROGRAMME
Coursework in Singapore

PRACTICAL KNOWLEDGE
Compulsory Internship & Dissertation

GLOBAL PROSPECTS
Internationally Recognized Degree

INTAKE
August Every Year

TO APPLY
Apply online from 1st November at www.tum-asia.edu.sg

1 TUM is ranked as the #1 University in Germany*

1 NTU is ranked #1 in Asia for Engineering**

1 NTU is ranked #1 in the world for industry income and innovation*

11 TUM ranked #11 in the Global Employability Survey*

50 Both TUM & NTU# are in the world’s Top 50 Universities
Inaugurated in 1991, Nanyang Technological University (NTU) has grown to become a full-fledged research university, and is ranked as one of the fastest-rising Asian universities in the world's top 50**. Hailing from more than 70 countries, NTU’s 3,800 strong teaching and research staff contribute their dynamic perspectives and years of solid industry experience. NTU’s academic and research programmes, with real-world relevance, have reaped dividends in the form of strong support from major corporations and industry leaders, in terms of both research funding and partnerships as well as global internship opportunities for our students.

As the main Science and Technology university in Singapore, NTU has made substantial contributions to Singapore’s drive for research and innovation, with the 2014 Quacquarelli Symonds (QS) ranking NTU at 10th in the World for Electrical & Electronic Engineering.

** As rated by 2013/2014 QS World University Ranking

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Technische Universität München (TUM)

Founded in 1868, Technische Universität München (TUM) has long established itself as a premier institute of higher learning in Germany. TUM has produced 13 Nobel Laureates and is ranked as Germany’s #1 University* for 3 consecutive years in the Shanghai Rankings. Having earned itself the reputation of being an institute that produces world-changing technologies, TUM Asia was set up in 2002 to bring German excellence to Asia.

Today, TUM Asia offers standalone and joint Bachelor and Master programmes in Singapore together with our partner universities such as National University of Singapore (NUS), Nanyang Technological University (NTU) and Singapore Institute of Technology (SIT).

Moving forward, TUM Asia strives to launch more Master and Bachelor programmes that continue to be industry-focused and in tune with the needs of the global economy.

* As ranked in 2011, 2012 & 2013 Shanghai Rankings (Academic Ranking of World Universities)
Master of Science
Integrated Circuit Design

The joint TUM-NTU Master of Science in Integrated Circuit Design (MSc in ICD) equips students with the academic proficiency and hands-on knowledge required in the design, development, and manufacture of integrated circuit or integrated electronic products.

**COURSE OUTLINE**

14

The student has to complete 14 modules in 2 semesters
(9 Core Modules, 4 Elective Modules, Cross Discipline Modules)

45

Contact hours for every Core & Elective Module

8

Technical Core Modules required to be completed by every student

**JOINT DEGREE**

Conferred by Technische Universität München (Germany) and Nanyang Technological University (Singapore)

**3 SEMESTERS**

Full-time research and application focused programme, inclusive of internship experience and Master Dissertation writing

**INDUSTRY RELEVANCE**

Our professors are actively involved in research and cooperation projects with leading industrial companies, allowing them to base the curriculum around the latest technological trends and knowledge

**GLOBAL OPPORTUNITIES**

You are able to complete your Internship and Dissertation in Munich, Singapore, or anywhere in the world, to look for job opportunities globally

**Duration of the Programme: 20 months**

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<th>5 Months</th>
<th>6 Months</th>
<th>3 Months</th>
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<td>Arrival in Singapore</td>
<td>• Business &amp; Technical English</td>
<td>• Core Modules</td>
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<td>Master Dissertation at NTU/TUM, a relevant company, or research institute</td>
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<td>• Core Modules</td>
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Module Synopsis

Core Modules
Digital IC Design

Analog IC Design

System-on-Chip Solutions & Architecture
The course addresses application-specific digital CMOS integrated circuits (ASICs) from a system’s perspective. Covering multiple levels of abstraction from MOSFET transistor, to realization of combinational / sequential logic, finite state machines (FSM), memory (SRAM, DRAM, FLASH), to chip-level interconnect technology (busses, network-on-chip (NoC)). Furthermore, the course complements the understanding of digital integrated circuit design by investigating the architectural composition of multiple real-world case studies taken from existing SoC products in the area of Internet networking. We will investigate and compare generic RISC CPU platform architectures with network processor units (NPUs), LAN/SAN (Local area / System area network) switches and SONET/SDH transmission framers of wide area networks (WAN).

Design Methodology & Automation
Computer-aided design of integrated circuits. VLSI design flow overview: system level, algorithmic level, register transfer level, logic level, and circuit level. Detailed discussion of VLSI design methods especially for logic synthesis. Digital simulation, hardware description language, test development including design for testability. Techniques from discrete mathematics and computer science are explained and employed.

Digital Signal Processing

Mixed Signal Circuit Design

Laboratory 1 Analog IC Design
Design of a wide-band amplifier from schematic to layout verification.

Laboratory 2 Digital IC Design
Design, simulation, layout and synthesis of digital integrated circuits & systems.

Elective Modules (Choose 4)
RF IC Design
System design considerations. CMOS RF components and devices. Low-noise amplifier (LNA), Mixers, Voltage-controlled oscillators (VCOs), RF power amplifiers. Phase-Locked Loops and Frequency Synthesizers.

IC Packaging
Packaging overview. Electrical packaging design and thermal management. Single chip, multichip and 3D packaging. IC assembling, sealing and encapsulation. IC packaging failure and reliability. Microsystems packaging and applications.

IC Marketing/ Business/ Management
Trends in the IC industry: technology and manufacturing trends, demand applications and product trends. Market characteristics: the customers, business cycles, demand lead and supply lag (the bull-whip effect), IC industry, supply and value chain, stakeholders, geographical distribution of excellence centers, technology centers, design centers, fabrication centers, the dis-integration of the value chain, outsourcing trends. Managing the marketing function: the sources of product ideas, the role of standards, formats, and intellectual property. Strategic partnership, distributorship, demand forecast, matching supply with demand.

Advanced MOSEFT & Novel Devices
Historical development of mainstream MOSFETs until today: economical, technological and physical fundamentals. Properties of long channel and short channel MOSFETs, high-field effects, scaling rules. Basics of charge carrier transport, drift-diffusion, Boltzmann-Bloch equation, hydrodynamic transport, ballistics and consequences for IV-characteristics. Advanced MOSEFTs, mobility-enhancement, metal-gate, FinFETs, MuGFETs. Hot-electron and ballistic transistors, Impact-MOSFETs, Spintronic devices. Tunneling-MOSFETs, single-electron transistors.

Nano-Electronics

Embedded Systems

Simulation and Optimization of Analog Circuits

Cross Discipline Modules
- Aspects of Asian and European Relations Today
- Cultural, Social & Economical Aspects of Globalisation
- International Intellectual Property Law
- Selected Topics in Business Administration
- Selected Topics in Business Management
**APPLICATION FEE**

| S$70 (inclusive of GST) or Euro 45 is payable for each application per programme | A Total of SGD 34,240 * (Actual fee split into SGD and EURO) |

Tuition fees includes teaching fees, laboratory expenses and cost of mandatory events. Expenses excluded from this fee and to be borne by students include: airfare, accommodation, living expenses, and NTU miscellaneous fees, inclusive of registration, IT facilities, matriculation, examination, amenities, copy right, sports, insurance and medical. The tuition fee will be paid in 3 installments.

* Tuition fees are accurate as of 1 September 2014. Tuition fees are subject to revision due to currency fluctuations, at the discretion of TUM Asia. Fees quoted are inclusive of 7% Singapore’s Government Goods & Services Tax. Please refer to [www.tum-asia.edu.sg/MScfees](http://www.tum-asia.edu.sg/MScfees) for the latest tuition fees.
Globalization is now an inevitable force that is here to stay. At TUM Asia, our classroom reflects this diversity with an enrolment of over 28 nationalities. This means that we foster a vibrant learning environment where the student learns not only from the textbook but also through the lives of their counterparts. Classroom ideas are synthesized across the diverse economic realities and students learn to see from multiple vantage points, creating a capacity to solve problems in creative ways. The unique 20 month joint degree equips the student with not only technical and scientific knowledge, but with an enriched curriculum composed of business and cultural modules.

Our students are not just learning about Integrated Circuit Design. The joint TUM-NTU ICD Master programme equips you to become an excellent independent engineer for the rest of your professional life. We teach you to master the challenges!

Dr. Helmut Graeb
Professor at Technische Universität München, Institute for Electronic Design Automation
DID YOU KNOW THAT A PART OF YOUR GADGET - COMPUTER, MOBILE PHONE, TABLET OR VIDEO CONSOLE - WAS DESIGNED OR MANUFACTURED IN SINGAPORE?

Electronics: The Core of Singapore’s Progress

Since the 1960s, Singapore’s electronics industry has been a major contributing sector to the nation’s manufacturing output. Over the years, the electronics sector has continued to flourish and move up the manufacturing chain as companies started to pour into higher-end functions such as Research & Development (R&D). Singapore aims to become a world class innovation-driven electronics hub, providing technology, manufacturing and business solutions and enabling the development of new growth areas.

The Semiconductor Industry in Singapore

The global industry being dominated by USA, South Korea, Japan, Taiwan, Singapore, and European Union. Every year, chip makers and designers dramatically increase the performance of their products while decreasing prices, making high-end technology goods increasingly productive and affordable for consumers. Singapore’s semiconductor industry has the highest growth potential and is currently the fastest growing industry sector in the country.

Our Graduates

Our graduates in Integrated Circuit Design are employed all over the world, such as in Singapore (77.6%), Europe (13.8%), India (8.6%)

The most commonly accepted positions are Integrated Circuit Design Engineer, Research Associate, Engineer, and PhD Researcher

Our graduates are expected to have a high amount of job opportunities due to strong partnerships with the industry. Companies that we partner closely with include Infineon, Lantiq, and STMicroelectronics

1. Today, Singapore accounts for 1 out of 10 wafer starts in the world

3. Three of the world’s top six outsourced semiconductor assembly and test companies are located in Singapore

15. 15 of the world’s top 25 fabless semiconductor companies, and close to 40 integrated circuit design centres are located in Singapore

20. Today, Singapore is home to approximately 20 semiconductor assembly and test operations

25. Electronics is the major industry underpinning Singapore’s economic growth, it contributes 25% of the total manufacturing value-add

40. 40% of the global hard disk manufactured in Singapore

The programme offered a module that was related to intellectual properties and invited a German professional patent attorney to teach it. The module opened a new window for me and helped me to discover a career path that I was passionate about.

Minghui Sun
Alumni, Master of Science in Integrated Circuit Design Technology Specialist, SHUSAKU·YAMAMOTO

When I look at the MSc in ICD interns, I see highly motivated individuals with a strong desire to learn and acquire new knowledge and skills. That mentality is very important in this dynamic industry.

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As rated by Academic Ranking of World Universities (Shanghai Ranking) 2011, 2012 and 2013 and 2012/2013 QS World University Ranking
As rated by Academic Ranking of World Universities (Shanghai Ranking) 2014
As rated by the Times Higher Education University Ranking 2013/2014
As ranked in the 2013 Global Employability Survey by The New York Times
As ranked by Academic Ranking of World Universities (Shanghai Ranking) 2013 and 2013/2014 QS World University Ranking